

Name: \_\_\_\_\_

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**UNIVERSITY OF CALIFORNIA**  
**College of Engineering**  
**Department of Electrical Engineering and Computer Sciences**

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**Final**  
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**EECS 240**  
**SPRING 2005**

*Show derivations and mark results with box around them. Erase or cross-out erroneous attempts. Simplify algebraic results as much as possible! Mark your name and SID at the top of the exam and all extra sheets.*

You may need the following integrals:

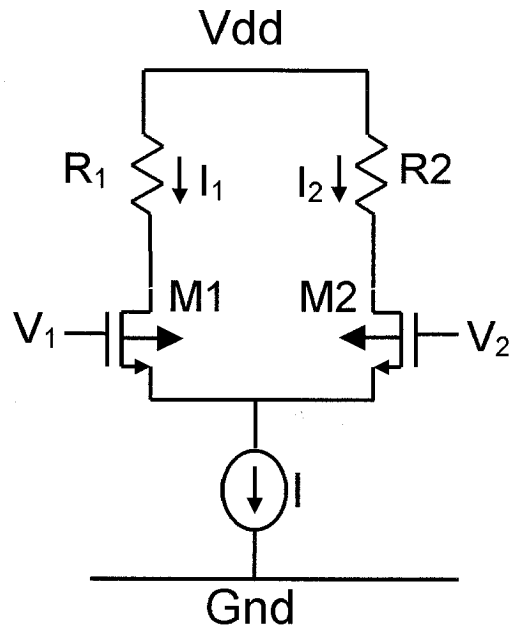
$$\int_0^{\infty} \left| \frac{1}{1 + \frac{s}{\omega_o}} \right|^2 df = \frac{\omega_o}{4}$$

$$\int_0^{\infty} \left| \frac{1}{1 + \frac{s}{\omega_o Q} + \frac{s^2}{\omega_o^2}} \right|^2 df = \frac{\omega_o Q}{4}$$

$$\int_0^{\infty} \left| \frac{\frac{s}{\omega_o}}{1 + \frac{s}{\omega_o Q} + \frac{s^2}{\omega_o^2}} \right|^2 df = \frac{\omega_o Q}{4}$$

	<b>Total pts</b>	<b>Score</b>
<b>Q1</b>	<b>20</b>	
<b>Q2</b>	<b>20</b>	
<b>Q3</b>	<b>30</b>	
<b>Q4</b>	<b>20</b>	
<b>Q5</b>	<b>10</b>	
<b>Total</b>	<b>100</b>	

Question 1 (20pts)



In the fully-differential amplifier above:

- a. (10pts) Calculate the input referred offset voltage for  $\Delta V_{TH}=2\text{mV}$ ,  $\Delta(W/L)/(W/L)=1\%$ ,  $\Delta R/R=1\%$ . M1 and M2 have  $V^*=200\text{mV}$ .
- b. (10pts) Assume only the following mismatches exist (different than the part a):  $\Delta R/R=1\%$ ,  $\Delta g_{mb}/g_{mb}=1\%$ . Calculate the CMRR.  $g_{mb}=0.1g_m$

$$a) \quad \Delta V_o = \Delta I \cdot R + \Delta R \cdot I$$

$$\Delta I = I \cdot \frac{\Delta W/L}{W/L} + g_m \cdot \Delta V_{TH}$$

$$\Delta V_{in} = \frac{\Delta V_o}{g_m R} \quad \frac{V_o}{V_{in}} = g_m \cdot R$$

$$\Delta V_{in} = \frac{R}{g_m R} \left[ I \cdot \frac{\Delta W/L}{W/L} + g_m \cdot \Delta V_{TH} \right] + \frac{\Delta R \cdot I}{g_m \cdot R}$$

$$\Delta V_{in} = \frac{I}{g_m} \cdot \frac{\Delta W/L}{W/L} + \Delta V_{TH} + \frac{I}{g_m} \cdot \frac{\Delta R}{R}$$

$$\frac{I}{g_m} = \frac{V^*}{2} \Rightarrow \Delta V_{in} = 100\text{mV} \cdot 0.01 + 2\text{mV} + 100\text{mV} \cdot 0.01$$

$$\Delta V_{in} = 4\text{mV} //$$

$$b) \quad V_{od}|_{cm} = \Delta g_{mb} \cdot R \cdot V_{cm} + \Delta R \cdot g_{mb} \cdot V_{cm}$$

We ignore:

$$A_{d-cm} = \Delta g_{mb} \cdot R + \Delta R \cdot g_{mb} = \frac{V_{od}}{V_{cm}}$$

$$\Delta R \cdot \frac{g_m}{g_m R_{tail} + 1}$$

↑  
Small

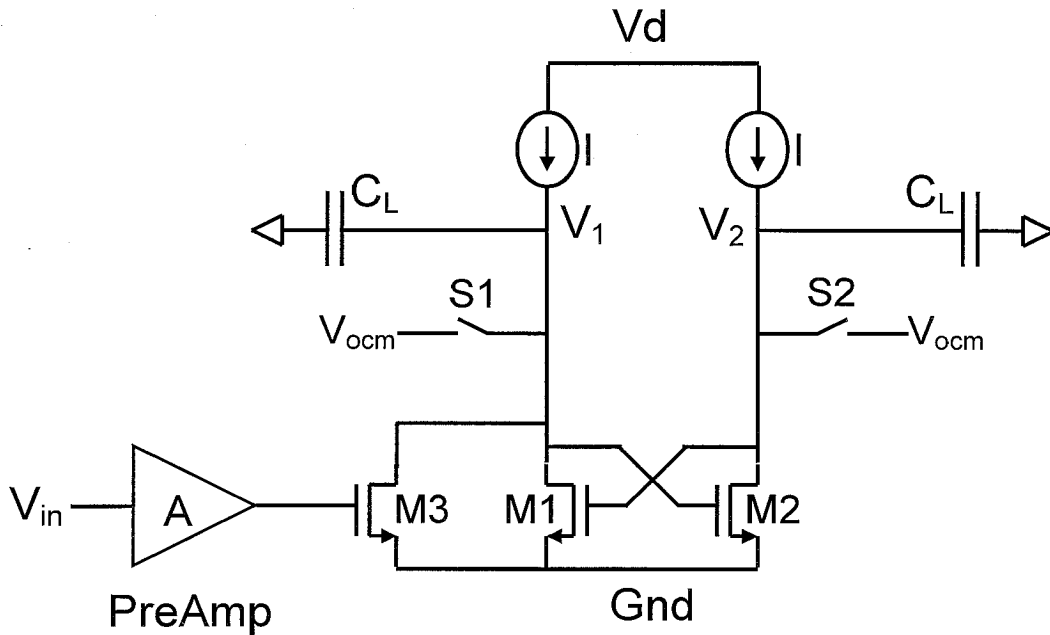
$$A_d = \frac{V_{od}}{V_{id}} = g_m \cdot R$$

$$CMRR = \frac{A_d}{A_{d-cm}} = \frac{g_m \cdot R}{\Delta g_{mb} \cdot R + \Delta R \cdot g_{mb}} = \frac{1}{\frac{\Delta g_{mb}}{g_m} + \frac{\Delta R}{R} \cdot \frac{g_{mb}}{g_m}}$$

$$g_{mb} = 0.1 g_m \Rightarrow \Delta g_{mb} = 0.001 g_m$$

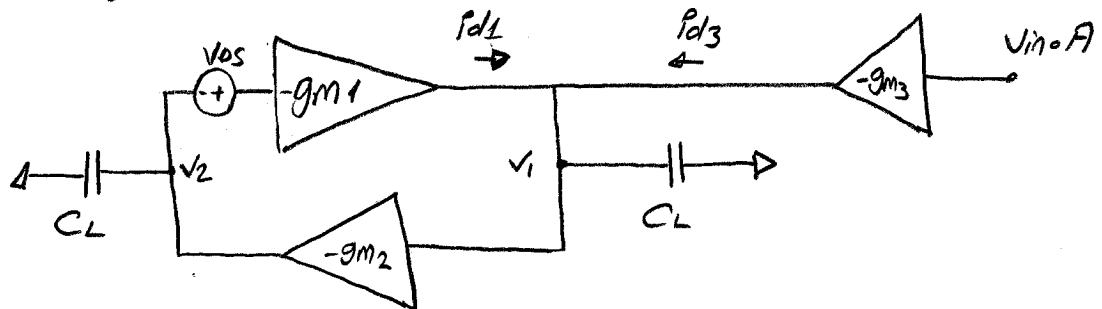
$$CMRR = \frac{1}{0.001 + 0.001} = 500 = 54 \text{ dB} //$$

Question 2 (20pts)



In the comparator circuit shown above,  $V_1$  and  $V_2$  are initialized to  $V_{ocm}$ . Due to the implementation errors switch S2 introduces an offset voltage of  $V_{os}$ . Given the offset voltage  $V_{os}$ , what is the minimum  $V_{in}$  that this comparator can successfully detect?

Small signal model:



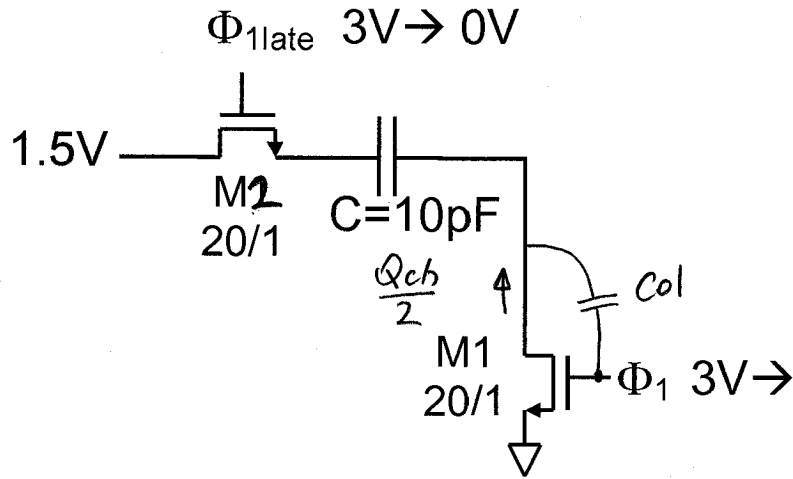
- $v_2$  &  $v_1$  are initialized to 0 : small-signal
- For  $V_{os} < 0$ ,  $I_{d1}$  cancels  $I_{d3}$ .  $I_{d1}$  must be smaller than  $I_{d3}$  for the correct transition.

$$I_{d1} < I_{d3}$$

$$V_{os} \cdot g_{m1} < g_{m3} \cdot A \cdot V_{in}$$

$$V_{os} \cdot \frac{g_{m1}}{g_{m3}} \cdot \frac{1}{A} < V_{in} \Rightarrow \text{Pre-amp decreases the influence of the } V_{os}.$$

Question 3 (20pts)



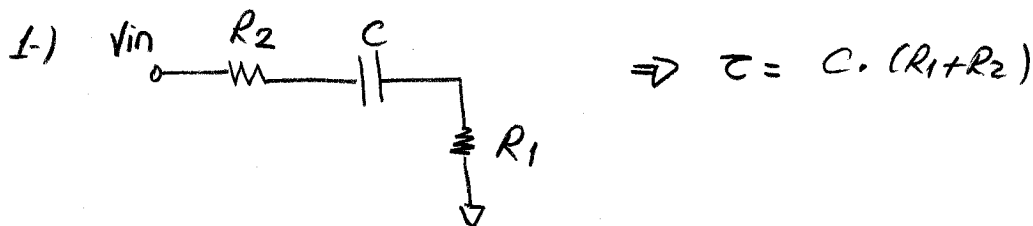
In the sampling circuit above, find the total sampling error for a sampling time of  $t_s = 25\text{ns}$ . Assume fast gating and 50% charge split. Hint:  $e^{-4} \sim 0.02$ .

Parameter:

$$V_{THN} = 1V, \mu_n C_{ox} = 200 \mu\text{A}/\text{V}^2, C_{ox} = 5\text{fF}/\mu\text{m}^2, C_{ol} = 0.2\text{fF}/\mu\text{m}, C = 1\text{pF}.$$

Assume square-law and ignore the body-effect.

- Errors :
- 1-) Settling error
  - 2-) Charge injection
  - 3-) Overlap cap:  $C_{gd}$



$$R_1 = \frac{1}{\mu_n C_{ox} \left(\frac{W}{L}\right)_1 (V_{GS} - V_{TH})} = \frac{1}{200 \frac{\mu\text{A}}{\text{V}^2} \cdot 20 \cdot 2\text{V}} = 125 \Omega$$

$$R_2 = \frac{1}{\mu_n C_{ox} \left(\frac{W}{L}\right)_2 (V_G - V_S - V_{TH})} = \frac{1}{200 \frac{\mu\text{A}}{\text{V}^2} \cdot 20 \cdot 0.5\text{V}} = 500 \Omega \Rightarrow$$

$$\tau = 10 \text{ pF} \cdot 625 \Omega = 6.25 \text{ nsec}$$

$$\text{Settling error} = 1.5 \text{ V} \cdot e^{-\frac{t_s}{\tau}} = 1.5 \cdot e^{-\frac{25 \text{ ns}}{6.25 \text{ ns}}} = 1.5 \times e^{-4} = 1.5 \cdot 0.02$$

$$= 30 \text{ mV} //$$

2-) Charge injection : Bottom plate sampling  $\rightarrow$  we'll consider  $M_1$  only.

$$Q_{ch} = W_1 \cdot L_1 \cdot C_{ox} (V_{gs} - V_{TH})$$

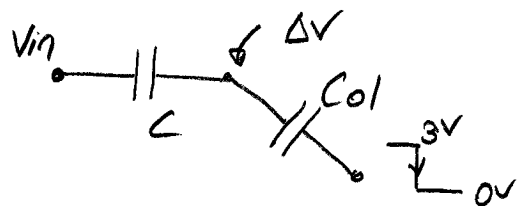
$$= 20 \mu\text{m} \cdot 1 \mu\text{m} \cdot \frac{5 \text{ fF}}{\mu\text{m}^2} \cdot (3 - 1) = 20 \cdot 5 \cdot 2 \text{ fF} \cdot \text{V}$$

$$= 200 \text{ fF} \cdot \text{V}$$

$$\text{Charge injection error} = \frac{Q_{ch}}{2} \cdot \frac{1}{C} = \frac{200 \text{ fF} \cdot \text{V}}{2} \cdot \frac{1}{10 \text{ pF}}$$

$$= 10 \text{ mV} //$$

3-) Overlap cap :



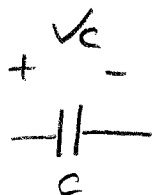
$$\Delta V \cong \frac{C_{ol}}{C} \cdot 3 \text{ V}$$

$$C_{ol} = W_1 \cdot L_1 \cdot C_{ol} = 20 \mu\text{m} \cdot 0.2 \frac{\text{fF}}{\mu\text{m}} = 4 \text{ fF}$$

$$\Delta V = \frac{4 \text{ fF}}{10 \text{ pF}} \cdot 3 \text{ V} = 1.2 \text{ mV} //$$

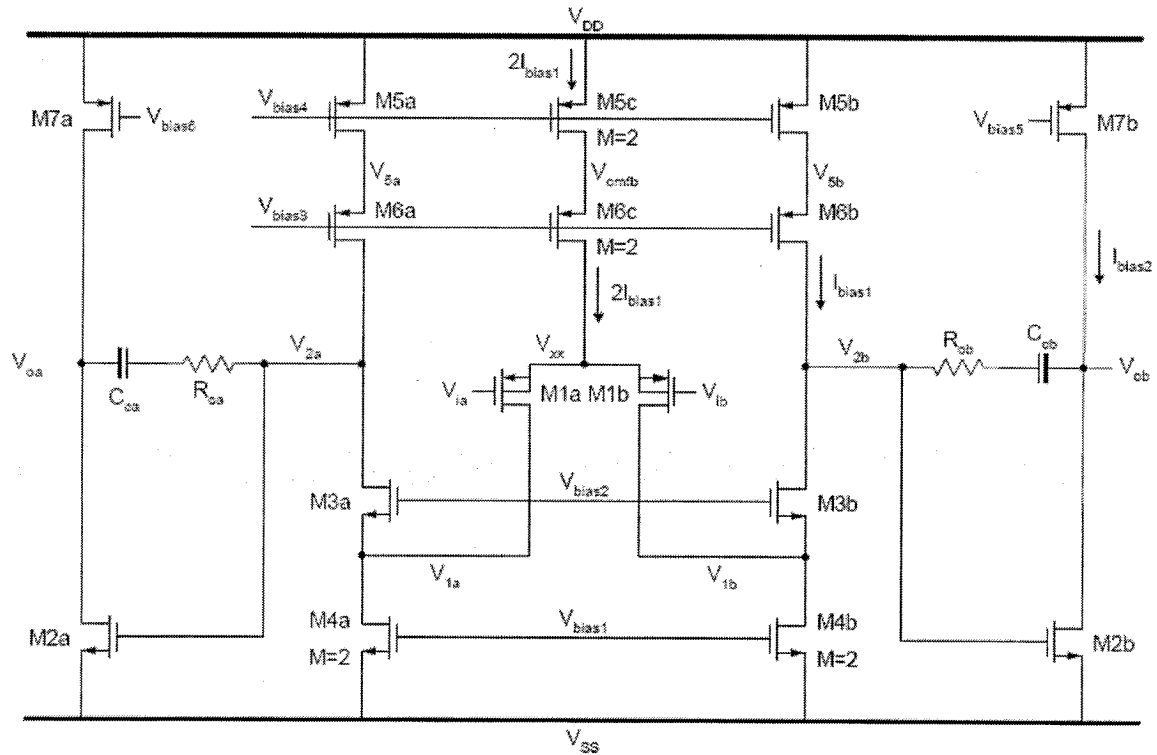
$$\text{Total sampling error} = -30 \text{ mV} + 10 \text{ mV} + 1.2 \text{ mV}$$

$$= -18.8 \text{ mV}$$



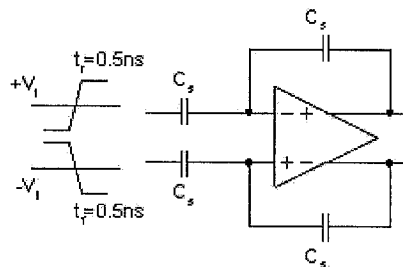
Charge injection and overlap cap increases  $V_c$ , while settling error decreases it.

**Question 4 (30pts)**



In the OTA above, all channel lengths are the same, all parasitic caps are 0 and  $2I_{bias1} = I_{bias2} = I$ .

- (10pts)** Calculate the ratio of  $W_{1a}/W_{2a}$  that sets the non-dominant pole to of the OTA to 4X the unity gain frequency  $\omega_u$  of the open loop OTA . You can assume the 2<sup>nd</sup> pole in the Miller compensation is at  $-[gm \text{ of the 2<sup>nd</sup> stage}]/C_c$ .
- (5pts)** Calculate the value of  $R_{ca}$ , in terms if the appropriate gm, that sets the RHP zero to infinity.
- (10pts)** Assuming the amplifier is in the feedback loop shown below, estimate the total output voltage noise in terms of  $V^*$ s. For this part you can ignore the effects of the non-dominant pole and the noise from the second stage.



- (5pts)** Using the feedback amplifier in part c, find the slewing time ( $t_{slew}$ ) when the amplifier is driven by a differential input of  $+8V_{fa}^*$ .



Q4

a) Miller compensation:

$$\omega_{-3dB} \approx \frac{g_{m1a}}{C_c} \quad p_2 = \frac{g_{m2a}}{C_c} \text{ (given)} \quad \frac{g_{m2a}}{C_c} = \frac{g_{m1a}}{4 C_c}$$

$$\frac{g_{m1a}}{g_{m2a}} = \frac{1}{4}$$

$$g_{m1a} = \sqrt{2 \cdot \left(\frac{W}{L}\right)_{1a} \mu_n I_{D1a}}$$

$$g_{m2a} = \sqrt{2 \left(\frac{W}{L}\right)_{2a} \mu_n I_{D2a}}$$

$$\frac{g_{m1a}}{g_{m2a}} = \sqrt{\frac{\left(\frac{W}{L}\right)_{1a} \cdot \frac{I_{D1a}}{I_{D2a}}}{\left(\frac{W}{L}\right)_{2a}}} = \frac{1}{4}$$

$$I_{D1a} = I/2 \quad I_{D2a} = I$$

$$L_{1a} = L_{2a}$$

$$\frac{W_{1a}}{W_{2a}} \cdot \frac{1}{2} = \frac{1}{16} \Rightarrow \frac{W_{1a}}{W_{2a}} = \frac{1}{8} //$$

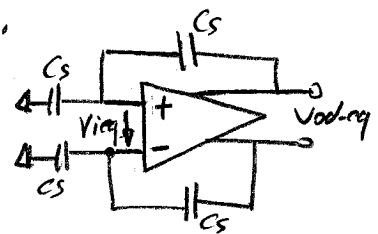
b)  $z = \frac{g_{m2a}}{C_c} \rightarrow$  with  $R_z \quad z = \frac{1}{\left(\frac{1}{g_{m2}} - R_c\right) C_c}$

$$z \rightarrow \infty \quad R_c = \frac{1}{g_{m2}} //$$

c)  $\frac{\overline{V_{ieq}^2}}{\Delta f} = \left(\frac{1}{g_{m1}}\right)^2 \cdot 4kT \gamma (g_{m1} + g_{m4} + g_{m5}) \cdot 2$   
 input equivalent noise

In the given feedback configuration  $F = \frac{1}{2}$ .

$$\left(\frac{\overline{V_{ieq}^2}}{\Delta f} \rightarrow \frac{\overline{V_{od-eq}^2}}{\Delta f}\right) = \left(\frac{1}{F}\right)^2 \cdot \left|\frac{1}{1 + \frac{s}{\omega_{uF}}}\right|^2$$



$$\frac{\overline{V_{od-eq}^2}}{\Delta f} = \underbrace{\left(\frac{1}{g_{m1}}\right)^2 \cdot 4kT \gamma (g_{m1} + g_{m4} + g_{m5}) \cdot 2}_{\frac{\overline{V_{ieq}^2}}{\Delta f}} \cdot \underbrace{\left(\frac{1}{F}\right)^2 \left|\frac{1}{1 + \frac{s}{\omega_{uF}}}\right|^2}_{\left|\frac{V_{od}}{V_{id}}\right|^2}$$

$$\text{Noise integral} = \frac{\omega_{00} F}{4} = \frac{1}{4} \frac{g_{m1}}{C_c} \cdot F$$

$$\Rightarrow \overline{V_{od-eq}}^2 = 2 \left( \frac{1}{g_{m1a}} \right)^2 4kT \gamma g_{m1a} \left( 1 + \frac{g_{m4a}}{g_{m1a}} + \frac{g_{m5a}}{g_{m1a}} \right) \cdot \left( \frac{1}{F} \right)^2 \cdot \frac{1}{4} \frac{g_{m1a}}{C_c} \cdot F$$

$$\overline{V_{od-eq}}^2 = 2 \cdot \frac{kT}{C_c} \cdot \frac{1}{F} \cdot \gamma \left( 1 + \frac{g_{m4a}}{g_{m1a}} + \frac{g_{m5a}}{g_{m1a}} \right)$$

$$g_{m4a} = \frac{2 \cdot I}{V_4^*}$$

$$g_{m1a} = \frac{2 \cdot I/2}{V_1^*}$$

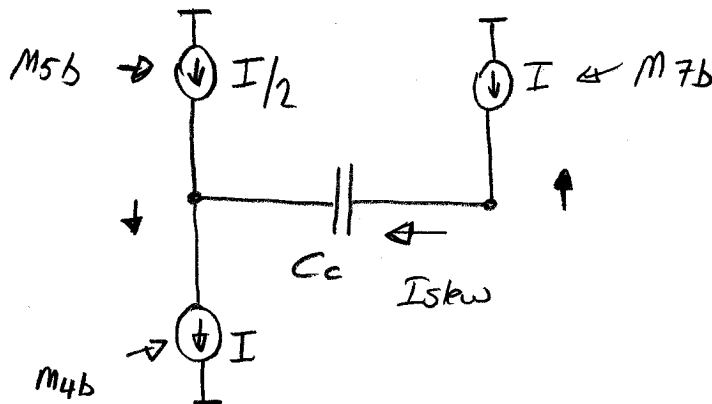
$$\overline{V_{od-eq}}^2 = 2 \cdot \frac{kT}{C_c} \cdot \frac{1}{F} \cdot \gamma \left( 1 + 2 \cdot \frac{V_{1a}^*}{V_{4a}^*} + \frac{V_{1a}^*}{V_{5a}^*} \right)$$

$$\frac{g_{m4a}}{g_{m1a}} = 2 \cdot \frac{V_1^*}{V_4^*}$$

d)  $\Delta V_{od} = 8 V_{1a}^*$   
(Signal gain = 1)

$$\Delta V_{od-linear} = \frac{V_{10}^*}{F} = 2 V_{10}^* \quad (F = \frac{1}{2})$$

$$\Delta V_{od-slew} = 6 V_{10}^*$$



$I_{slew}$  is limited by the folded cascode and it is  $\frac{I}{2}$

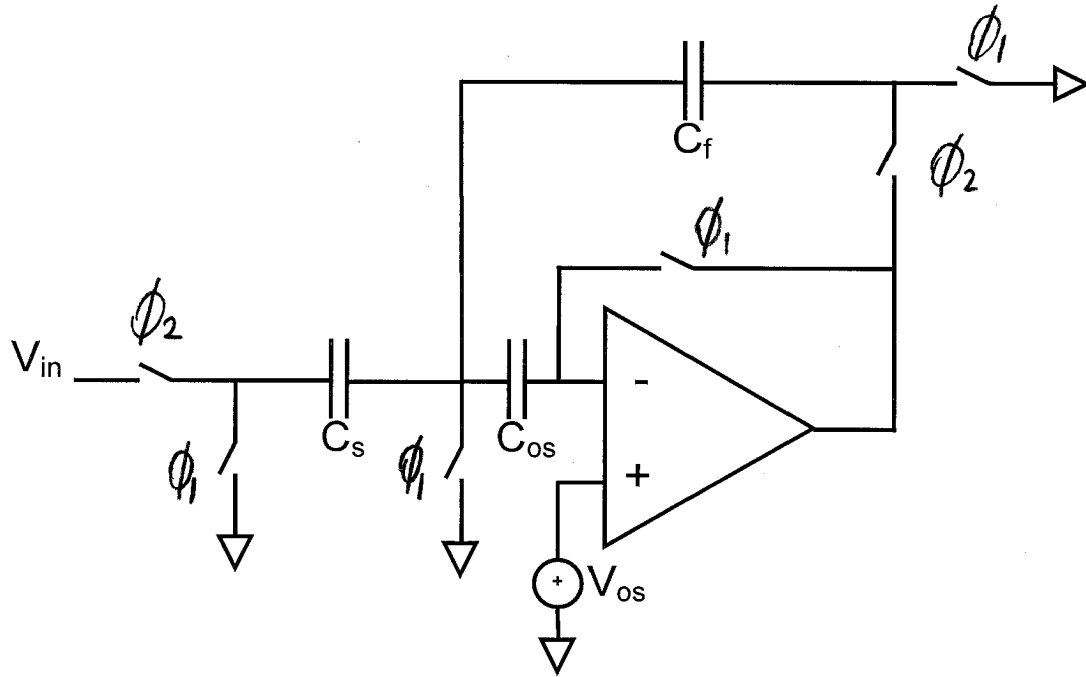
$$\frac{I}{2} \cdot t_{slew} \cdot \frac{1}{C_c} = 6 \cdot V_{10}^* \Rightarrow t_{slew} = 6 \cdot C_c \cdot \frac{V_{10}^*}{I/2 \triangleq I_{D1a}}$$

$$t_{slew} = 12 \cdot \frac{C_c}{g_{m1a}}$$

$$t_{slew} = \frac{12}{\omega_{u1}} //$$

open loop (not the loop gain)

Question 5 (10pts)



In the switch cap gain circuit above, there are two non-overlapping phases available:  $\Phi_1$ ,  $\Phi_2$ . In order to have an inverting gain stage with offset cancellation, assign appropriate phases ( $\Phi_1$  or  $\Phi_2$ ) to switches in the circuit.