

NAME and SID

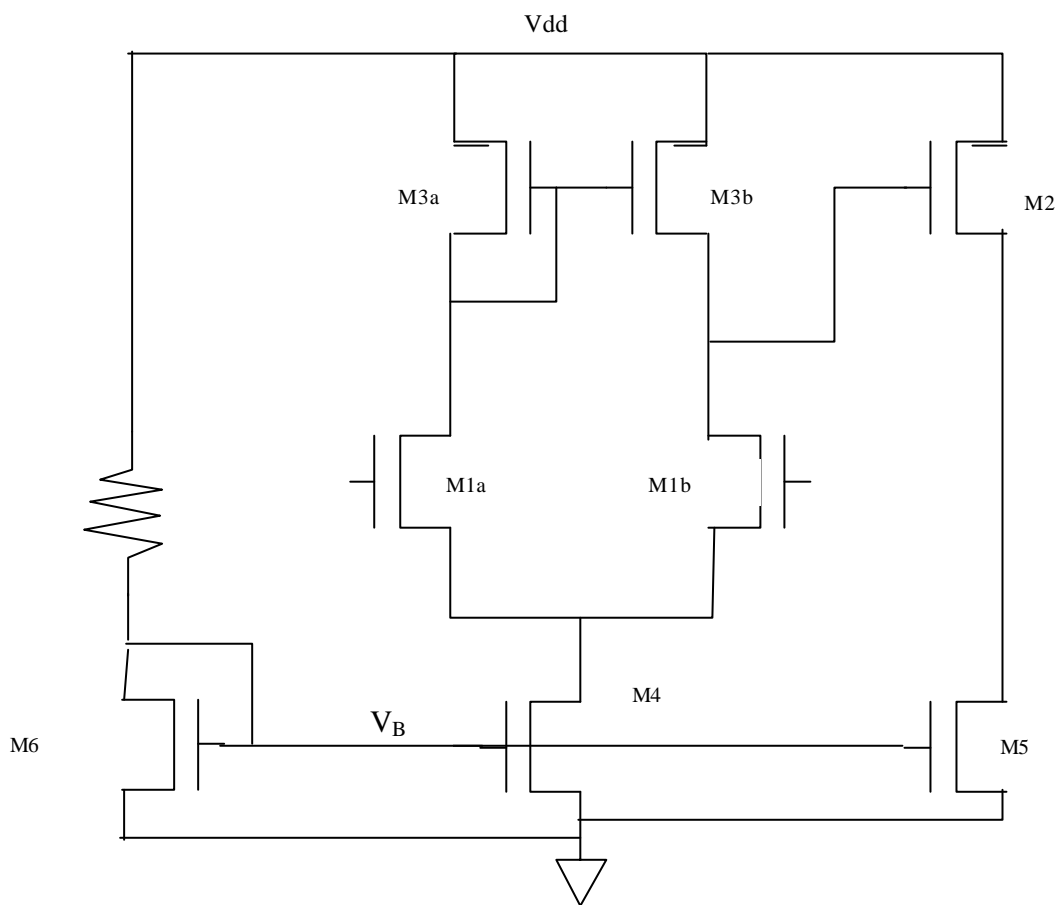
Note: Use the device parameters given in class. $V_t = \pm 0.5V$, $\mu_n C_{ox} = 200 \mu A/V^2$, $\mu_p C_{ox} = 100 \mu A/V^2$, $\lambda = 0.02$ 1/V

1. (30pts) Design an amplifier with a low frequency gain of at least 100 and a unity gain frequency of 5 GHz with a 10pF load. Use an NMOS-input common-source amplifier with a PMOS current source load. You may use one resistor in your circuit, and assume a 5V supply, but all other devices must be FETs (no ideal voltage source, current sources, etc.). Try to minimize the size WIDTH of your transistors. All overdrive voltages (V_{ov} or V_{dsat}) must be between 0.1 and 0.5 volts.

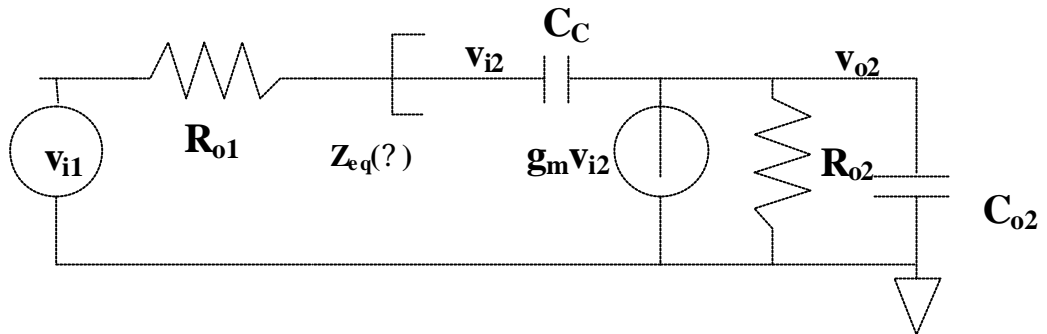
- ? Calculate the values for g_m , r_o , I_d , and V_{dsat} that you will use.
- ? Draw a schematic of your design and clearly label all components ($R = \dots$, $W/L = \dots$)
- ? Fill in the table with the data about each of your FETs.

Transistor	W	L	V_{dsat}	g_m	r_o

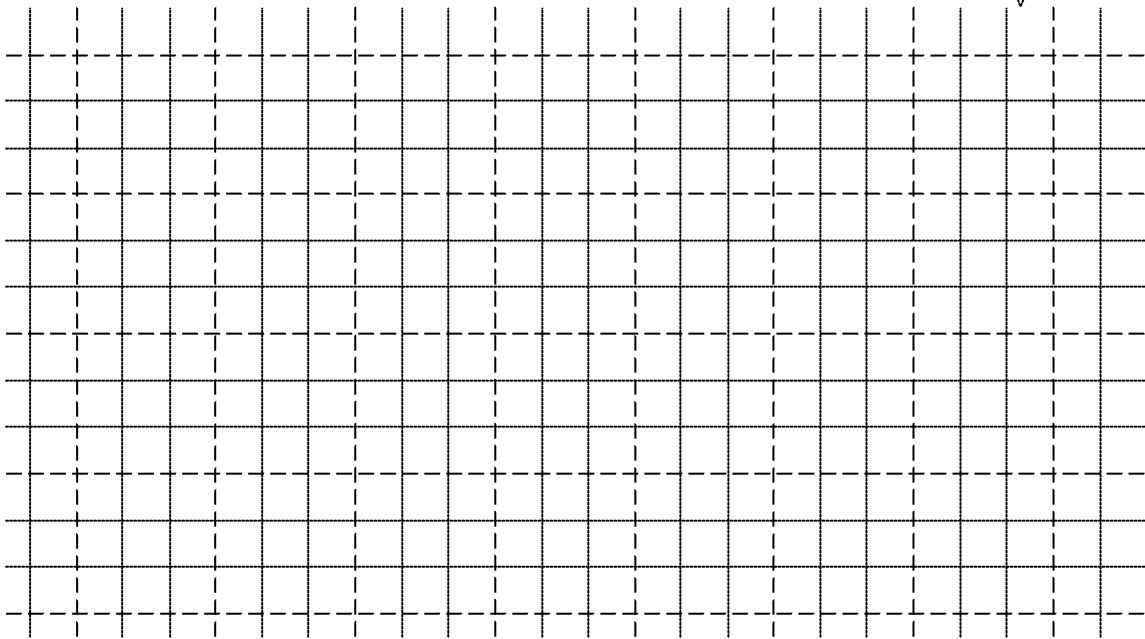
2. (25pts) For the amplifier shown below,
- ? Circle and label all differential pairs, current mirrors, gain stages.
 - ? Label the input(s) and the output(s)
 - ? Which devices make up the bias network:
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- ? Which transistors are in the signal path:



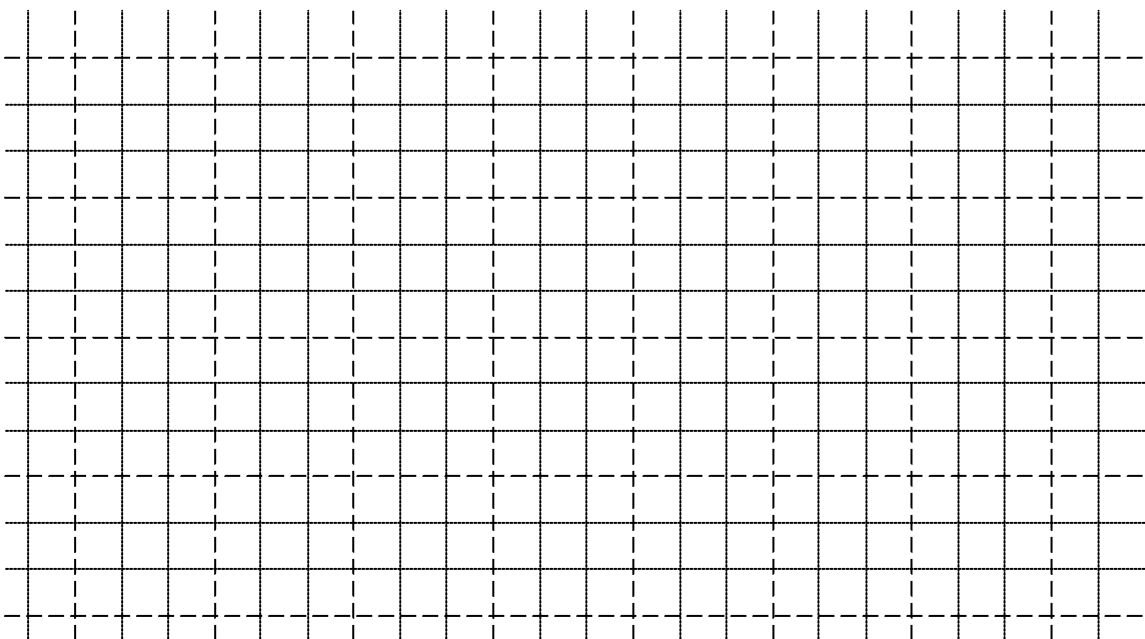
3) (30pts) For the amplifier below assume that $g_m=500\mu\text{S}$, $R_{o1}=100\text{k}\Omega$, $R_{o2}=200\text{k}\Omega$, $C_{o2}=1\text{pF}$, $C_c=0.1\text{pF}$. In the top plot draw the magnitude of the impedance $Z_{eq}(?)$ seen looking into the capacitor at node V_{i2} , and in the lower plot draw the magnitude of the first stage gain, v_{i1} to v_{i2} , and the total gain, v_{i1} to v_{o2} . LABEL AXES, poles, and magnitudes CLEARLY!



$Z_{eq}(?) |$



$A_{v1} |$
 $A_{v1} * A_{v2} |$



4. (15 points)

- A. You have a single stage MOS amplifier with a low frequency gain of 100, a pole frequency of 5MHz, and an output capacitance of 1pF. Calculate the unity gain frequency, the transconductance g_m , and the output resistance R_o .
- B. You need to design a single-stage amplifier with a gain of 5 at 10^9 rad/sec, and a DC gain of 50. Calculate the unity gain frequency, the pole frequency, and the gain at 10^7 rad/sec and 10^{10} rad/sec.
- C. You have a single-stage amplifier with an output resistance of 10^7 Ohms, a transconductance of 10mS, and a unity gain frequency of 10^9 rad/sec. What is the DC gain, the pole frequency, and the output capacitance?