

# Fall 2015 Midterm-2

1. Answer the following questions concisely. Use the semiconductor parameters in the following Table if needed.

Electron diffusion coefficient	$D_n$	30 cm <sup>2</sup> /s
Hole diffusion coefficient	$D_p$	10 cm <sup>2</sup> /s
Electron mobility	$\mu_n$	1200 cm <sup>2</sup> /s-V
Hole diffusion coefficient	$\mu_p$	400 cm <sup>2</sup> /s-V
Electron diffusion length	$L_n$	2 μm
Hole diffusion length	$L_p$	1 μm

- Two NPN BJT's have identical doping levels. The base width of BJT-A is half that of BJT-B. Under the same base-emitter bias voltage, which BJT has higher collector current?
- What is the **ratio** of the collector currents in NPN and PNP BJT's if they have identical dimensions and doping concentrations (but opposite doping types)?
- What is the **ratio** of the transconductances ( $g_m$ ) of NMOS and PMOS if they have identical dimensions, doping concentrations, magnitudes of threshold voltages and gate-to-source bias voltages?

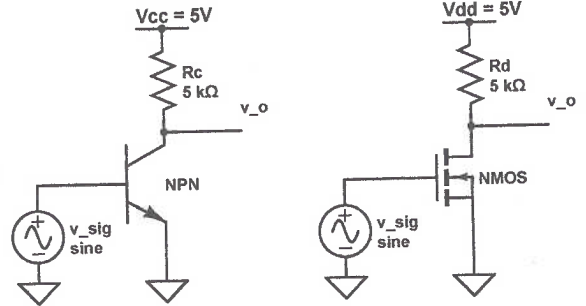
(a)  $I_c = I_s e^{V_{BE}/V_T}$ ,  $I_s = \frac{q A E D_n n_i^2}{N_A W} \propto \frac{1}{W}$   
 So BJT-A will have 2x higher collector current.

(b)  $\frac{I_c(\text{NPN})}{I_c(\text{PNP})} = \frac{D_n}{D_p} = \frac{30}{10} = 3$

(c)  $I_D(\text{NMOS}) = \frac{k_n}{2} (V_{GS} - V_{tn})^2$ ,  $g_m(\text{NMOS}) = k_n (V_{GS} - V_{tn})$   
 $I_D(\text{PMOS}) = \frac{k_p}{2} (|V_{GS}| - |V_{tp}|)^2$ ,  $g_m(\text{PMOS}) = k_p (|V_{GS}| - |V_{tp}|)$

$\frac{g_m(\text{NMOS})}{g_m(\text{PMOS})} = \frac{k_n}{k_p} = \frac{\mu_n C_{ox}}{\mu_p C_{ox}} = \frac{1200}{400} = 3$

2. Consider the two amplifiers shown below (only the AC circuit is shown). The NPN BJT has a current gain of 100, and a  $v_{CE,sat} = 0.3V$ , and the NMOS has  $k_n = 1 \text{ mA/V}^2$  and  $V_{tn} = 1V$ . Ignore Early effects. Assume both transistors are biased at  $0.5 \text{ mA}$ .



- Find the voltage gains of both amplifiers. Which amplifier has higher gain?
- Which amplifier (BJT or NMOS) has higher input resistance? What's its value?
- What are the output resistances of both amplifiers?
- Which amplifier (BJT or NMOS) has large output swing? What's its value? (Note: output swing is defined as the smaller of the upward and downward voltage swings)

$$I_C = I_D = 0.5 \text{ mA}$$

$$\text{BJT: } g_m = \frac{I_C}{V_T} = 40 \times 0.5 = 20 \text{ mS}$$

$$\text{MOS: } I_D = \frac{1}{2} k_n V_{ov}^2 = 0.5 \text{ mA. } k_n = 1 \text{ mA/V}^2$$

$$\Rightarrow V_{ov} = 1 \text{ V}$$

$$g_m = k_n V_{ov} = 1 \text{ mS}$$

$$(a) A_v(\text{BJT}) = -g_m R_C = -100 \text{ V/V}$$

$$A_v(\text{MOS}) = -g_m R_D = -5 \text{ V/V}$$

$$(b) R_{in}(\text{BJT}) = r_{\pi} = \frac{\beta}{g_m} = 100 \times 50 = 5 \text{ k}\Omega$$

$$R_{in}(\text{MOS}) = \infty \Rightarrow \text{larger}$$

$$(c) R_o(\text{BJT}) = R_C = 5 \text{ k}\Omega$$

$$R_o(\text{MOS}) = R_D = 5 \text{ k}\Omega$$

$$(d) \text{BJT: } V_C = 5 - I_C R_C = 2.5 \text{ V}$$

$$\text{Upward swing } V(+)= V_{CC} - V_C = 2.5 \text{ V}$$

$$\text{downward " } V(-)= V_C - V_{CE,sat} = 2.5 - 0.3 = 2.2 \text{ V}$$

$$\text{Output swing} = \pm 2.2 \text{ V}$$

(Continuation of Problem 2 solution)

$$\text{MOS: } V_D = V_{DD} - I_D R_D = 2.5V$$

$$\text{Upward swing} = v(+)= V_{DD} - V_D = 2.5V$$

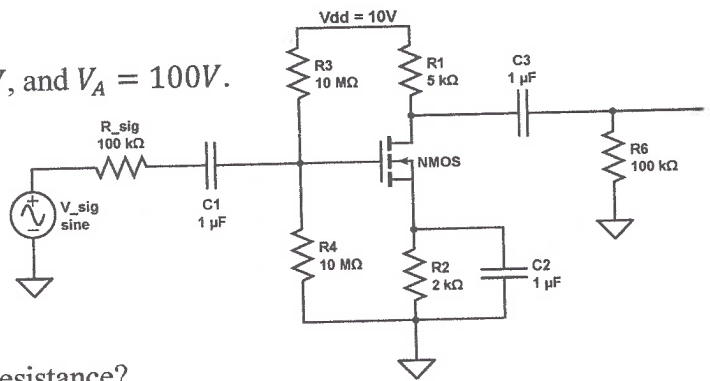
$$\text{Downward " } v(-) = V_D - V_{D,\text{sat}} = 2.5 - V_{OV} = 1.5V$$

$$\text{Overall swing} = \pm 1.5V$$

⇒ BJT Amplifier has larger output swing

3. The NMOS below has  $k_n = 1 \text{ mA/V}^2$ ,  $V_{tn} = 1 \text{ V}$ , and  $V_A = 100 \text{ V}$ .

- What is the amplifier configuration?
- Find the DC bias current and drain-source voltage. You can assume  $V_A = \infty$  for DC analysis.
- Find the small signal parameters,  $g_m$ ,  $r_o$ .



- What is the overall gain of the amplifier including the signal resistance and load resistance?
- What is the maximum output voltage swing? (Note: output swing is defined as the smaller of the upward and downward voltage swings)

(a) Common Source Amplifier

$$(b) I_D = \frac{1}{2} k_n (V_{GS} - V_{tn})^2 = \frac{V_S}{R_2}$$

$$V_{GS} = V_G - V_S$$

$$V_G = V_{dd} \cdot \frac{R_4}{R_3 + R_4} = 5 \text{ V}$$

$$\Rightarrow \frac{1}{2} \cdot 1 \cdot (4 - V_S)^2 = \frac{V_S}{2} \Rightarrow V_S^2 - 8V_S + 16 = V_S$$

$$\Rightarrow V_S = 2.44 \text{ V} \quad (\text{the other solution of } 6.6 \text{ V} \text{ leads to cut-off of MOS})$$

$$I_D = 1.22 \text{ mA} \# \quad V_D = 10 - I_D \cdot 5 \text{ k}\Omega = 3.9 \text{ V} \quad V_{DS} = 1.46 \text{ V} \#$$

$$(c) g_m = \frac{2I_D}{V_{ov}} = \frac{2.44}{(5 - 2.44 - 1)} = 1.56 \text{ mS} \#$$

$$r_o = \left( \frac{I_D}{V_A} \right)^{-1} = 82 \text{ k}\Omega \#$$

↑  
See note at the end of the solution

$$(d) G_V = \frac{-R_{in}}{R_{sig} + R_{in}} \cdot A_{v0} \cdot \frac{R_L}{R_o + R_L} = 0.98 (-g_m) \underbrace{(R_1 \parallel R_6 \parallel r_o)}_{4.5 \text{ k}\Omega}$$

$$R_{in} = R_3 \parallel R_4 = 5 \text{ M}\Omega$$

$$G_V = -6087 \text{ V/V}$$

(Continuation of Problem 3 solution)

(e)  $V_D = 3.9V$

Upward swing =  $V_{DD} - V_D = 10 - 3.9 = 6.1V$

Downward swing =  $3.9 - 4 = -0.1$

↑

$V_G - V_{tn} = 4V$

⇒ No swing

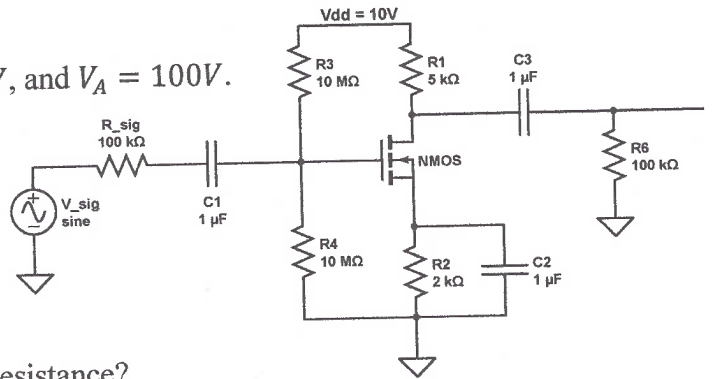
(The circuit is not well designed!)

The MOSFET is not in saturation,

$V_{DS} < V_{OV}$

# \*\* Corrected Problem \*\*

3. The NMOS below has  $k_n = 0.5 \text{ mA/V}^2$ ,  $V_{tn} = 1\text{V}$ , and  $V_A = 100\text{V}$ .
- What is the amplifier configuration?
  - Find the DC bias current and drain-source voltage. You can assume  $V_A = \infty$  for DC analysis.
  - Find the small signal parameters,  $g_m$ ,  $r_o$ .
  - What is the overall gain of the amplifier including the signal resistance and load resistance?
  - What is the maximum output voltage swing? (Note: output swing is defined as the smaller of the upward and downward voltage swings)



(a) Common Source

$$(b) I_D = \frac{1}{2} k_n (V_{gs} - V_{tn})^2 = \frac{V_S}{R_2}, \quad V_{gs} = V_G - V_S = V_{DD} \frac{R_4}{R_3 + R_4} - V_S$$

$$\Rightarrow \frac{1}{2} \cdot \frac{1}{2} \cdot (5 - V_S - 1)^2 = \frac{V_S}{2}$$

$$\Rightarrow (4 - V_S)^2 = 2V_S \Rightarrow V_S^2 - 8V_S + 16 = 2V_S$$

$$V_S = 2\text{V} \quad (8\text{V is unphysical)}$$

$$I_D = 1\text{mA}$$

$$V_{DS} = V_{DD} - I_D (R_1 + R_2) = 3\text{V} > V_{ov} = V_{gs} - V_{tn} = (5 - 2) - 1 = 2\text{V}$$

$$(c) g_m = \frac{2I_D}{V_{ov}} = \frac{2}{2} = 1\text{mS}$$

$$r_o = \frac{V_A}{I_D} = 100\text{k}\Omega$$

$$(d) G_v = - \frac{R_{in}}{R_{sig} + R_{in}} (g_m) (R_1 \parallel r_o \parallel R_6) = -4.4 \text{ V/V}$$

$$R_{in} = R_3 \parallel R_4 = 5\text{M}\Omega$$

$$(e) V(+)=V_{DD}-V_D = I_D R_D = 5\text{V}$$

$$V(-) = V_D - V_{ov} = 5 - 2 = 3\text{V}$$

$$\Rightarrow \text{Output swing} = \pm 3\text{V}$$

4. The NPN BJT has a current gain of 100. Assume Early voltage  $V_A = \infty$ .
- What is the amplifier configuration?
  - What is the input resistance (not including  $R_L$ )?
  - What is the output resistance,  $R_{out}$  (including  $R_{sig}$ )?
  - What is the overall voltage gain, including the signal and load resistance?
  - What is the overall current gain of the amplifier, i.e., the ratio of the current flowing  $R_L$  and current from source?

a) Common base

b)  $R_{in} = r_e \approx g_m^{-1} = \boxed{26 \Omega}$

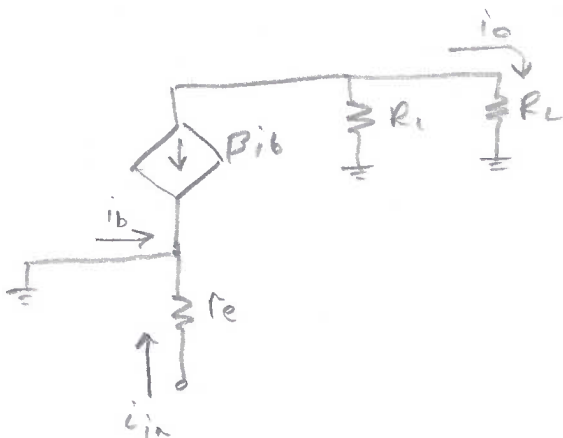
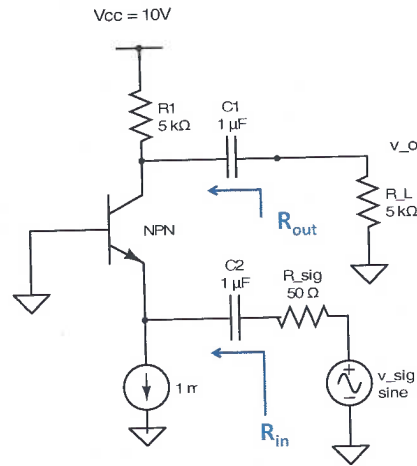
c)  $R_{out} = 5 k\Omega$

d)  $G_v = \frac{R_{in}}{R_{sig} + R_{in}} g_m (R_L \parallel R_C)$

$$= \frac{26}{50 + 26} (0.04)(2.5k)$$

$G_v = \boxed{34 \text{ V/V}}$

e)  $G_i = \frac{R_{sig}}{R_{in} + R_{sig}} A_i$        $A_i = \frac{i_o}{i_{in}}$



$$i_o = -\beta i_b \left( \frac{R_C}{R_C + R_L} \right)$$

$$i_{in} = -(\beta + 1) i_b$$

$$i_o = i_{in} \left( \frac{\beta + 1}{\beta} \right) \left( \frac{R_C}{R_C + R_L} \right)$$

$$A_i = \frac{i_o}{i_{in}} = \frac{R_C}{R_C + R_L} = \frac{5k}{5k + 5k}$$

$A_i = \boxed{0.5 \text{ A/A}}$  Full credit was given for this answer

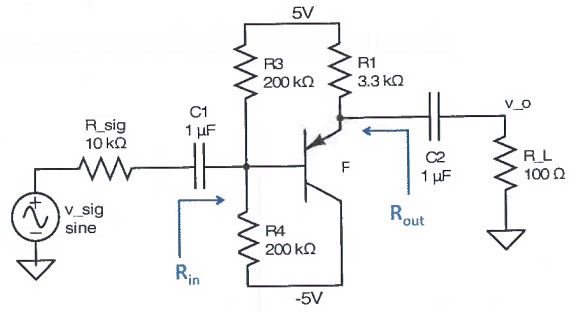
The question asked for overall current gain need to consider source resistance.

$$G_i = \frac{R_{sig}}{R_{in} + R_{sig}} A_i = \frac{50}{50 + 26} (0.5) = 0.33 \text{ A/A}$$



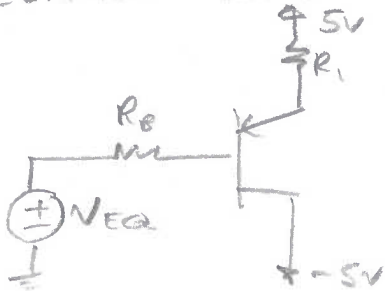
5. The PNP BJT has a current gain of 100. Ignore Early effect.

- What is the amplifier configuration?
- Find the DC bias point (collector current and collector-to-emitter voltage).
- What is the input resistance,  $R_{in}$  (not including  $R_L$ )?
- What is the output resistance,  $R_{out}$  (including the effect of  $R_{sig}$ )?
- What is the overall voltage gain including signal and load resistances?



a) Common collector (emitter follower)

b)



$$V_{BEQ} = 0$$

$$R_B = 200k // 200k = 100k\Omega$$

$$5V - I_E R_1 - V_{EB} - I_B R_B = 0$$

$$5V - I_E R_1 - V_{EB} - \frac{I_C R_B}{\beta} = 0$$

$$\approx 5V - I_C R_1 - V_{EB} - \frac{I_C}{\beta} R_B = 0$$

$$I_C \left[ -R_1 - \frac{R_B}{\beta} \right] = -4.3$$

$$I_C = 1mA$$

$$V_{CE} = -(5 - 3.3 + 5V)$$

$$V_{CE} = -6.7V$$

$$c) R_{in} = (\beta + 1)(r_e + R_1) // R_B$$

$$= (101)(26 + 3300) // 100k$$

$$R_{in} = 77k\Omega$$

$$d) R_{in} \text{ (including } R_L) = (\beta + 1)(r_e + R_1 // R_L) // R_B$$

$$= (101)(26 + 97) // 100k$$

$$= 11k\Omega$$

$$G_v = \frac{R_{in}}{R_{sig} + R_{in}} \frac{R_L // R_1}{R_L // R_1 + r_e} = \left( \frac{11k}{11k + 10k} \right) \left( \frac{97}{97 + 25} \right)$$

$$= 0.42 V/V$$