

University of California College of Engineering Department of Electrical Engineering and Computer Sciences

E. Alon, G. Ranade, B. Ayazifar, C. Tomlin, V. Subramanian Thurs., Apr. 9, 2015 2:00-3:30pm

EECS 16A: SPRING 2015—MIDTERM 2

Important notes: Please read every question carefully and completely – the setup may or may not be the same as what you have seen before. Also, be sure to show your work since that is the only way we can potentially give you partial credit.

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- Problem 3: ____/ 22
- Problem 4: ____/ 28
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PROBLEM 1. MEMS Sensors (13 pts)

We saw in lecture that our smart phones use capacitance measurement based techniques to enable the touchscreen, but it turns out that many of the other capabilities that make a smart-phone "smart" rely on capacitance measurement techniques as well. In particular, to first order, all of the motion sensors in the phone (accelerometer, gyroscope, etc.) use capacitance measurement to electrically sense the position of some (small) movable object (this is mechanical parts of "MEMS", which stands for Micro-Electro-Mechanical System), where the position (and/or overall trajectory) of that object is directly influenced by the type of motion we are trying to detect.

In this problem we'll examine the highly simplified sensor shown below. When the whole device is accelerated to the left or to the right at a certain rate, the "proof" mass will shift to the left or the right by a distance Δx proportional to the acceleration.



a) (3 pts) Assuming that no acceleration is applied (i.e., $\Delta x = 0$), that H=10µm, L=100µm, W = 20µm, d = 0.5µm, and that the proof mass is made out of nickel (i.e., a conductive metal), what is the capacitance between E1 and E2?

b) (4 pts) Keeping the other parameters the same as in part (a), then if $\Delta x = 10e-9 s^2 * a$ (where *a* is the acceleration with units of m/s²), write an equation relating the capacitance between E1 and E2 with the acceleration *a*.

c) (6 pts) Using any combination of ideal circuit elements you'd like to, design a circuit that produces a voltage that increases along with the capacitance between E1 and E2. (I.e., if C_{E1-E2} increases your output voltage V_{out} also increases, but you don't specifically need a purely linear relationship between C_{E1-E2} and V_{out} .)

PROBLEM 2. Communication with Noise and Interference (28 points)

You and your friend Alice have set up your own wireless communication system where Alice transmits to you messages that consist of a sequence of length 4 whose values are ± 1 . Calling this signal x[n], you should assume that x[n] is 0 for n ≤ 0 and for n>4. Due to noise and interference from other wireless signals, your antenna and the circuit you use to amplify/record the signal produced by the antenna can't exactly reproduce the values of x[n], but you do have a circuit that computes the cross-correlation of x[n] with sequence y[n], where y[1] = 1, y[2] = -1, and y[3] = 1.

a) (6 pts) Alice sends you a message and the cross-correlation signal that you get c[n] is c[-2] = -1, c[-1] = 2, c[0] = -1, c[1] = 1, c[2] = 0, c[3] = 1, with c[n] = 0 for all other values of n. Set up a system of equations relating the measured c[n] with the (still unknown) message x[n].

b) (4 pts) Show how to use the system of equations from part a) to estimate Alice's transmitted signal x[n].

c) (6 pts) You end up communicating with Alice quite often, and you notice that occasionally the output from your cross-correlation circuit is noisier than the first few times you tested it. For example, let's say you recorded a cross-correlation signal c[n] where c[-2] = -0.7, c[-1] = 1.8, c[0] = -2.8, c[1] = 3.3, c[2] = -2.1, c[3] = 1.3, with c[n] = 0 for all other *n*. In this case, would you change the strategy you use to estimate Alice's transmitted signal? If so, how would you change it? If not, why not?

d) (6 pts) On one particular occasion you are trying to communicate with Alice, but you happen to walk by a cell phone tower and notice that some of the values coming out of your cross-correlation circuit have been horribly corrupted (probably by the large interference created by the signals transmitted by the cell phone tower). In fact, you get c[-2] = 100, c[-1] = 100, c[0] = -2.5, c[1] = 3.3, c[2] = -2.1, c[3] = 100, and c[n] = 0 for all other values of n. Knowing that Alice is relatively far away and that her transmitter can't put out too large of a signal (after all, she doesn't want her battery to die), you decide to throw out some of the unreasonable values. Set up a new system of equations relating the measured c[n] with x[n], but with the unreasonable values thrown out. Please be sure to explicitly highlight which of the c[n] measurements you intend to throw out.

e) (6 pts) Having thrown out the unreasonable values of c[n] from part (d), and knowing that Alice would want to send a signal with as little energy in it as possible (again, she doesn't want her battery to die), now how you would you estimate Alice's transmitted signal x[n]?

PROBLEM 3. Multitouch Resistive Touchscreen (22 points + BONUS 16 pts)

In this problem we will look at a simplified version of the multitouch resistive touchscreen. In particular, rather than measuring the position of two potential touch points in both dimensions (i.e., a pair of coordinates (x_1, y_1) and (x_2, y_2) corresponding to two touch positions), let's think about a version where we are interested in measuring only the vertical position of the two touch points (i.e., y_1 and y_2). Therefore, unlike the touchscreens we looked at in class and as shown below, both of the resistive plates (i.e., both the top and the bottom plate) would have conductive strips placed along their top and bottom edges.



a) (4 pts) Assuming that both of the plates are made out of a material with $\rho = 1 \ \Omega^*m$ and that the dimensions of the plates are W = 3cm, H = 12cm, and T = 0.5mm, with no touches at all, what is the resistance between terminals E1 and E2 (which would be the same as the resistance between terminals E3 and E4)?

b) (6 pts) Now let's look at what happens when we have two touch points. Let's assume that at wherever height the touch occurs, a perfect contact is made between the top plate and the bottom plate along the entire width of the plates (i.e., you don't have to worry about any lateral resistors), but that otherwise none of the electrical characteristics of the plates change. Defining the bottom of the plate as being y = 0cm (i.e., a touch right at E1 would be at y = 0cm), let's assume that the two touches happen at $y_1 = 3$ cm and $y_2 = 7$ cm, and that your answer to part (a) was 5k Ω (which may or may not be the right answer), draw a model with 6 resistors that captures the electrical connections between E1, E2, E3, and E4. Note that for clarity, the system has been redrawn below to depict this scenario.



c) (6 pts) Using the same assumptions as part b), if you drove terminals E3 and E4 with a 1mA current source (as shown below) but left terminals E1 and E2 opencircuited, what is the voltage you would measure across E4-E3 (i.e., V_{E4-E3})?



d) (6 pts) Now let's try to generalize the situation by assuming that the two touches can happen at any two arbitrary points y_1 and y_2 , but with y_1 defined to always be less than y_2 (i.e., y_1 is always the bottom touch point). Leaving the setup the same as in part c) except for the arbitrary y_1 and y_2 , by measuring only the voltage between E4 and E3, what information can you extract about the two touch positions? Please be sure to provide an equation relating V_{E4-E3} to y_1 and y_2 as a part of your answer, and note that you may want to redraw the model from part (b) to help you with this.

e) (BONUS: 8 pts) One of your colleagues claims that by measuring the appropriate voltages, not only can they extract what both y_1 and y_2 are in this system, they can even do so in a way that would have a set of three independent voltage equations related to y_1 and y_2 and hence gain some robustness to noise in the voltage measurements.

In order to facilitate this, write equations relating V_{E4-E2} and V_{E1-E3} to y1 and y2. (The third voltage we'll use is V_{E4-E3} , which you should have already derived an equation for in the previous part of the problem.)

f) (**BONUS: 8 pts**) Using the equations you derived in part e), show the procedure you would use to estimate y_1 and y_2 . Note that your final result should be in the form of a matrix-vector equation that predicts y_1 and y_2 as a function of the measured voltages. (Note that you can plug in numbers for all of the dimensions/material constants such as H, L, etc.)

PROBLEM 4. IoT4eva, Again (28 pts)

In this problem we'll revisit yet again (we called the company "4eva" for a reason) some aspects related to powering the device being made by the start-up IoT4eva. Just like in HW6 and as shown below, the device will be powered by a super-capacitor (which is basically just a big capacitor) charged to some initial voltage $V_{Csc}(0) = V_{init}$, and we will use the voltage regulator shown below to provide a fixed 1.2V output voltage to the rest of the circuits in the device.



This time around we're going to be looking at how to build some circuits that will help us to keep track of how long the device will continue to operate off of the charge stored in the super-capacitor, or if it is about to die.

a) (6 pts) Assuming that the super-capacitor has a capacitance $C_{sc} = 10kF$ and an initial voltage $V_{init} = 1.5V$, how much charge can we pull out of the super-capacitor before its voltage gets too low to ensure that 1.2V comes out of the voltage regulator? In other words, we want to calculate the "usable charge" stored in the super-capacitor.

b) (10 pts) We'd like to know when the device is getting close to dying so that we can give a warning to the user to charge it. Using the same V_{init} as part a), design a circuit that outputs 0V once the super-capacitor has 10% or less of the charge you computed in part a) (i.e., the **usable charge**), and that outputs 1.2V otherwise. You are allowed to use ideal op-amps and resistors to build this circuit, the 1.2V coming from the regulator, the voltage on the super-capacitor itself (V_{Csc}), and one ideal voltage source whose voltage you can select. Please remember that as shown in the figure, the 1.2V voltage produced by the regulator is relative to the ground connected to the super-capacitor.

Hint: What is the voltage on the super-capacitor V_{Csc} when only 10% of its usable charge is left?

Note that in this part as well as in part c), partial credit will be provided for correct descriptions and/or block diagrams indicating the strategy you intend to use to meet the goals of the design.

c) (12 pts) Instead of only a binary indication of the super-capacitor being almost out of usable charge, we might instead want a voltage that is linearly proportional to the remaining usable charge. Under the same constraints as part b), design a circuit that outputs 1.2V when 100% of the usable charge is left, 0V when 0% of the usable charge is left, and that drops linearly from 1.2V for any percentage in between (e.g. 0.6V for 50%, 0.3V for 25%, etc.).

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