UNIVERSITY OF CALIFORNIA, BERKELEY College of Engineering Department of Electrical Engineering and Computer Sciences

EE 105: Microelectronic Devices and Circuits MIDTERM EXAMINATION #1

Spring 2013

Time allotted: 45 minutes

NAME: _____

STUDENT ID#:

INSTRUCTIONS:

- 1. Unless otherwise stated, assume
 - a. temperature is 300 K
 - b. material is Si
- 2. SHOW YOUR WORK. (Make your methods clear to the grader!) Specially, while using chart, make sure that you indicate how you have got your numbers. For example, if reading off mobility, clearly write down what doping density that corresponds to.
- 3. Clearly mark (underline or box) your answers.
- 4. Specify the units on answers whenever appropriate.

SCORE:1 / 15

2 / 15

Total _____730

PHYSICAL CONSTANTS

Description	<u>Symbol</u>	<u>Value</u>	
Electronic charge	q	1.6×10 ⁻¹⁹ C	
Boltzmann's constant	k	8.62×10 ⁻⁵	
		eV/K	
Thermal voltage at	$V_{\rm T} =$	0.026 V	
300K	kT/q		
$V_{\rm T} \ln(10) = 0.060 \text{ V}$ at $T=300 \text{ K}$			

PROPERTIES OF SILICON AT 300K

<u>Description</u>	<u>Symbol</u>	Value
Band gap energy	E_{G}	1.12 eV
Intrinsic carrier	ni	10^{10} cm^{-3}
concentration		
Dielectric permittivity	$\varepsilon_{ m Si}$	1.0×10^{-12}
		F/cm

Electrostatics: $\frac{dE}{dx} = \frac{\rho}{\varepsilon}$ $E = -\frac{dV}{dx}$

Depletion region Width:
$$W = \sqrt{\frac{2\varepsilon}{q} \left(\frac{1}{N_a} + \frac{1}{N_d}\right) \left(V_{bl} - V_{Applied}\right)}$$

Reverse saturation current of a diode, $J_s = q n_i^2 \left(\frac{D_n}{N_A L_n} + \frac{D_p}{N_D L_p} \right)$

Electron and Hole Mobilities in Silicon at 300K



Prob 1. [15]

(a) [12 pts] A Si sample is doped first with B to 10¹⁷ /cm³.
(i) [2 pts] Find out the number of electrons and holes.

(b) [10 pts] Now the Si sample is further doped with As to 2x 10¹⁷ /cm³ and a voltage of 1 Volt is applied across the sample. Find out the amplitude of the current that will flow due to this voltage. Assume that the sample has the following dimensions: Length=10 μm, Width=1μm and thickness=1μm.

(c) [3 pts] What is the mechanism of current flow in a p-n junction diode under (i) forward and (ii) reverse bias. Simply mention the mechanisms.

Prob 2: [15 pts]

(a) [3 pts] For the following p-n junction diode, roughly sketch the

(i) charge density profile

(ii) electric field profile

(iii) energy band diagram

across the junction.

 $N_A = 10^{18} / cm^3$ $N_D = 10^{18} / cm^3$

(b)[8 pts] A p-n junction has to be designed such that the width of the depletion region on the N side is 0.212 μ m, the built in potential is 0.72 V and the maximum electric field at the junction is $3.4x10^4$ V/cm. What doping density on the p and n side will have to be used to design such a diode? What is the capacitance of this diode?

(c) [4 pts] What are the different mechanisms of reverse bias breakdown in a p-n junction diode? What are the main physical mechanisms responsible for each of these breakdown mechanisms?

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