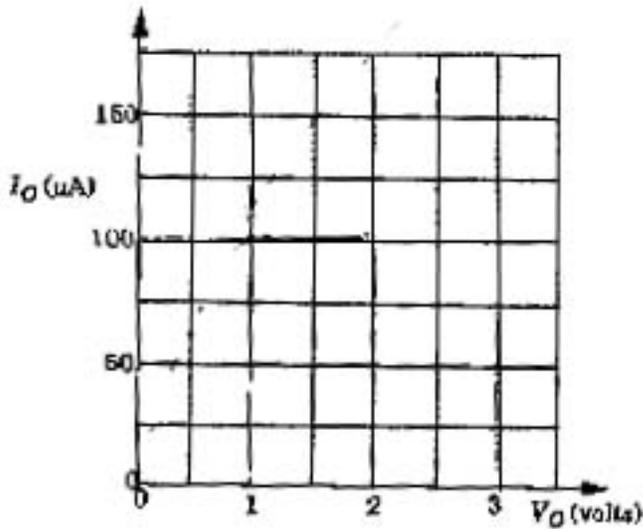
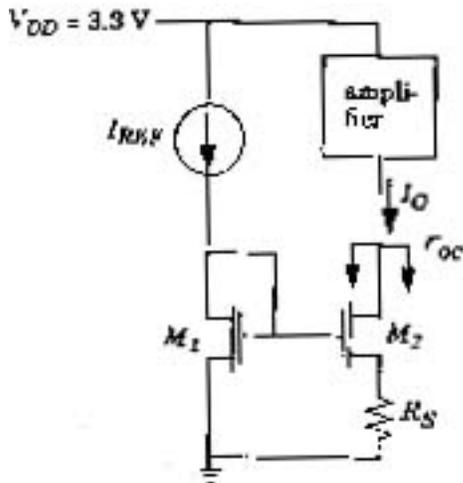


Plot the output current I_O versus the output voltage V_O (which should vary from 0 to 3.5 V). There is no need for accuracy in the triode region; however, the numerical value for the saturation current and the boundary between the triode and the saturation region should be correct.



Problem #1d[3 pts]

For the new current source below, we would like to keep the same value for I_O in saturation as you found in part (a). In order to do this, we must change the width-to-length ratio of M_1 . Note that the reference current I_{ref} is unchanged. Find the numerical value of the width of M_1 , given that its length is $L_1 = 2 \mu\text{m}$. Again, you can assume for this part that $I_O = 50 \mu\text{A}$, in case you were not able to solve part (a).

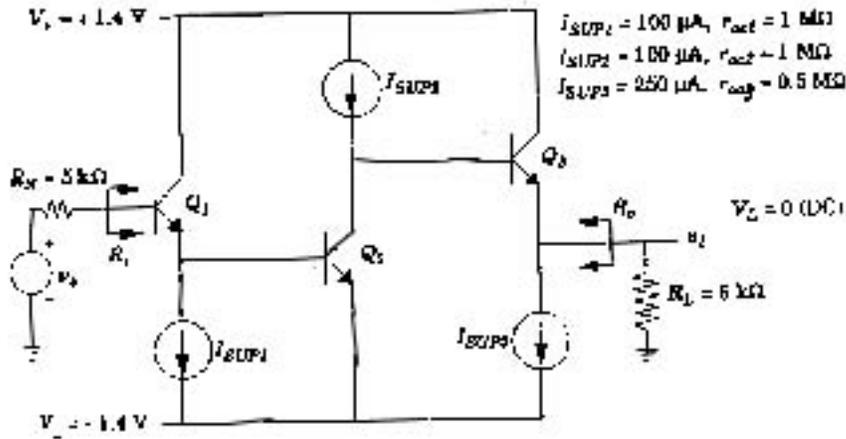


Problem #1e[3 pts]

What is the numerical value of the output resistance of the new current source? Again, you can assume for this part that $I_O = 50 \mu\text{A}$, in case you were not able to solve part (a).

Problem #2a[3 pts]

Find the numerical value of VCE for each transistor. Note that the DC output voltage $V_L = 0V$. You can assume that the transistors are forward-active and that $V_{BE} = 0.7 V$.

**Problem #2b[5 pts]**

Find the numerical value of the input resistance, R_i .

Problem #2c[6 pts]

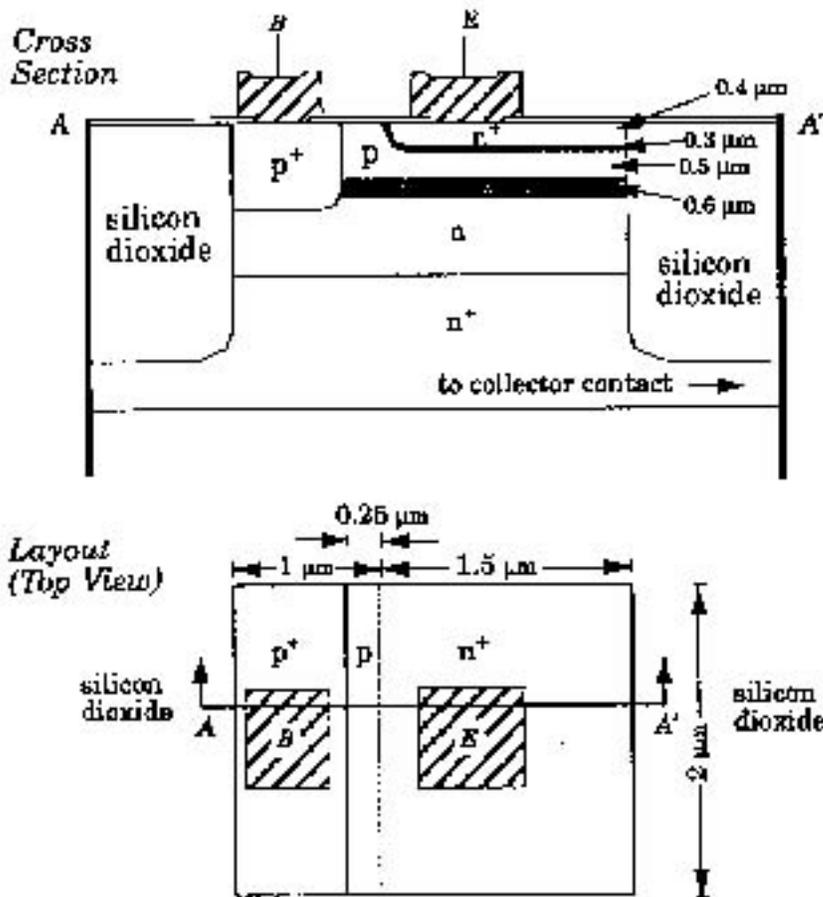
Find the numerical value of the output resistance, R_o .

Problem #2d[6 pts]

Find the numerical value of the overall small-signal voltage gain, A_v , including the loading effects of the source and load resistors: $A_v = v_L/v_s$.

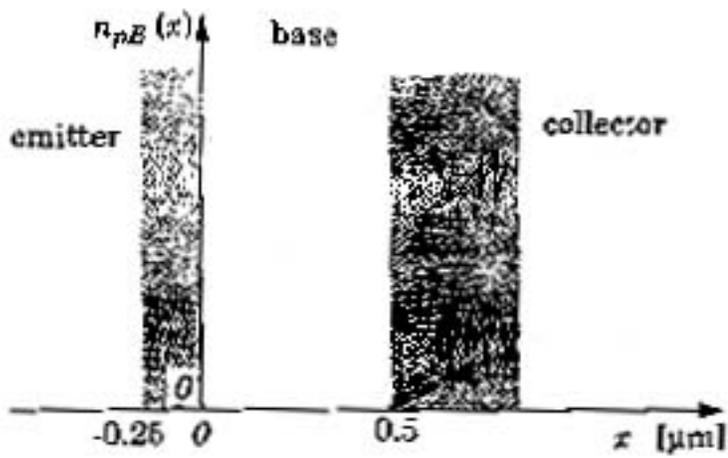
Problem #3a[5pts]

Note that the thicknesses of the emitter and the base regions W_e and W_b are given in the cross section, along with the thicknesses of the depletion regions between emitter and base, x_{BE} and between the base and collector x_{BC} . The base doping is $N_{aB} = 10^{16}$ per cubic cm. Given epsilon of silicon is 1.03×10^{-12} F/cm. What is the numerical value of the small-signal capacitor C_u between the base and collector?



Problem #3b[5 pts]

Given that the transistor is under forward-active bias with $V_{BE} = 650\text{mV}$ and $V_{CE} = 1.0\text{V}$. Sketch the electron concentration in the base, $n_p(x)$ on the graph below. The numerical values for the electron concentration should be accurate at $x=0$ (edge of the emitter-base junction) and at $x=0.5\mu\text{m}$ (edge of the base-collector junction).



**Posted by HKN (Electrical Engineering and Computer Science Honor Society)
University of California at Berkeley**
If you have any questions about these online exams
please contact <mailto:examfile@hkn.eecs.berkeley.edu>