

NAME (please print) _____

STUDENT (SID) NUMBER _____

UNIVERSITY OF CALIFORNIA

College of Engineering
Electrical Engineering and Computer Sciences
Berkeley

EECS 145M: Microcomputer Interfacing Lab

LAB REPORTS:

1 _____ (100 max)	8 _____ (100 max)	21 _____ (100 max)
2 _____ (100 max)	9 _____ (100 max)	22 _____ (100 max)
3 _____ (100 max)	10 _____ (100 max)	23 _____ (100 max)
* lowest grade dropped		25 _____ (100 max)

HIGHEST 8 LAB GRADES $\times 5/8$ _____ (500 max)

LAB PARTICIPATION _____ (100 max)

MID-TERM #1 _____ (100 max)

MID-TERM #2 _____ (100 max)

FINAL EXAM _____ (200 max)

TOTAL COURSE GRADE _____ (1000 max)

COURSE LETTER
GRADE

Spring 1994 FINAL EXAM (May 20)

Answer the questions on the following pages completely, but as concisely as possible. The exam is to be taken *closed book*. Use the reverse side of the exam sheets if you need more space. Calculators are OK but not needed. **In answering the problems, you are not limited by the particular equipment you used in the laboratory exercises.** Many formulae from the course have been provided for you on the last page.

Partial credit can only be given if you show your work.

FINAL EXAM GRADE :

1 _____ (42 max) 3 _____ (60 max) 5 _____ (16 max)

2 _____ (50 max) 4 _____ (32 max) TOTAL _____ (200 max)

Initials _____

Problem 1 (total 42 points):

Define the following terms (should take no more than 40 words)

a. (7 points) **Comparator circuit**

b. (7 points) **Harmonic** (of a periodic signal)

c. (7 points) **Data bus** (for connecting 2 or more parallel outputs)

Initials _____

Problem 1 (continued):

d. (7 points) **Differential Linearity Error** (of an A/D converter)

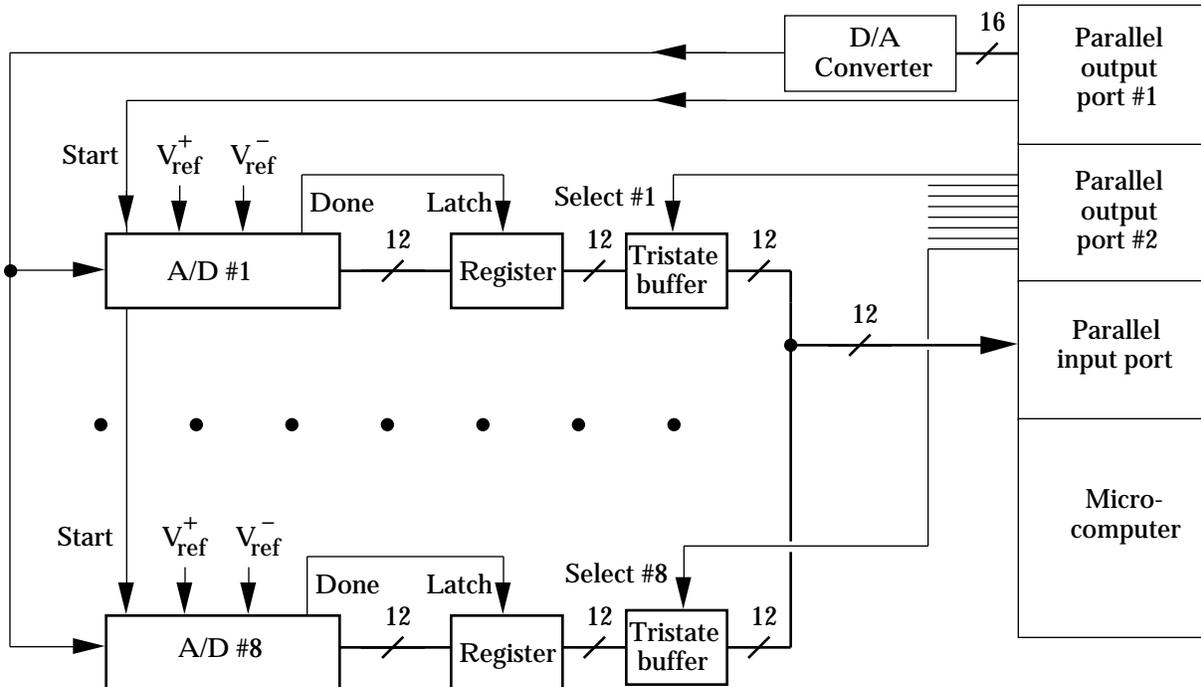
e. (7 points) **Fourier Convolution Theorem**

f. (7 points) **Spectral Leakage** (as seen in the FFT of a periodic signal)

Initials _____

Problem 2 (50 points)

The block diagram below shows a system for measuring the properties of eight 12-bit A/D converters automatically under computer control. The output of a single 16-bit D/A converter is used for all analog inputs. All eight A/Ds are started simultaneously and their outputs are latched onto 12-bit registers when they are done. The A/D conversion time is $10 \pm 1 \mu\text{s}$.



- a. (40 points) Describe the steps (both hardware and software) necessary to measure (i) the maximum absolute accuracy error, (ii) the minimum step size, and (iii) the maximum step size of each of the eight A/D converters. Assume that you know the reference voltages V_{ref}^+ and V_{ref}^- . (Note: there are over ten steps)

Initials _____

Problem 2 (continued)

- b.** (10 points) Assuming that the parallel port read and write operations each take $10\ \mu\text{s}$ and that the time to execute other program steps is negligible, how many seconds will the entire procedure take for the 8 A/D converters? (show work)

Initials _____

Problem 3 (60 points)

Imagine that many years ago, a spacecraft was sent to measure the magnetic fields in the great void between the sun and the nearest star. This information is transformed into an analog waveform that is exactly one second in duration and has frequencies extending from 1 Hz to 2,000 Hz. Because the spacecraft is far from earth and has limited battery power, the data signal is weak. However, there is a much larger background noise from the rest of the universe that adds to the weak data signal. This background noise is white (all frequencies are present with equal amplitude) and extends beyond 1 MHz.

To be able to detect the weak data signal, you use three techniques:

- (i) the spacecraft sends the 1 s data signal over and over again with a period of exactly 1 s
- (ii) the radio signal is low-pass filtered before sampling
- (iii) you use your knowledge of the FFT of a periodic signal to further separate the background noise from the data signal

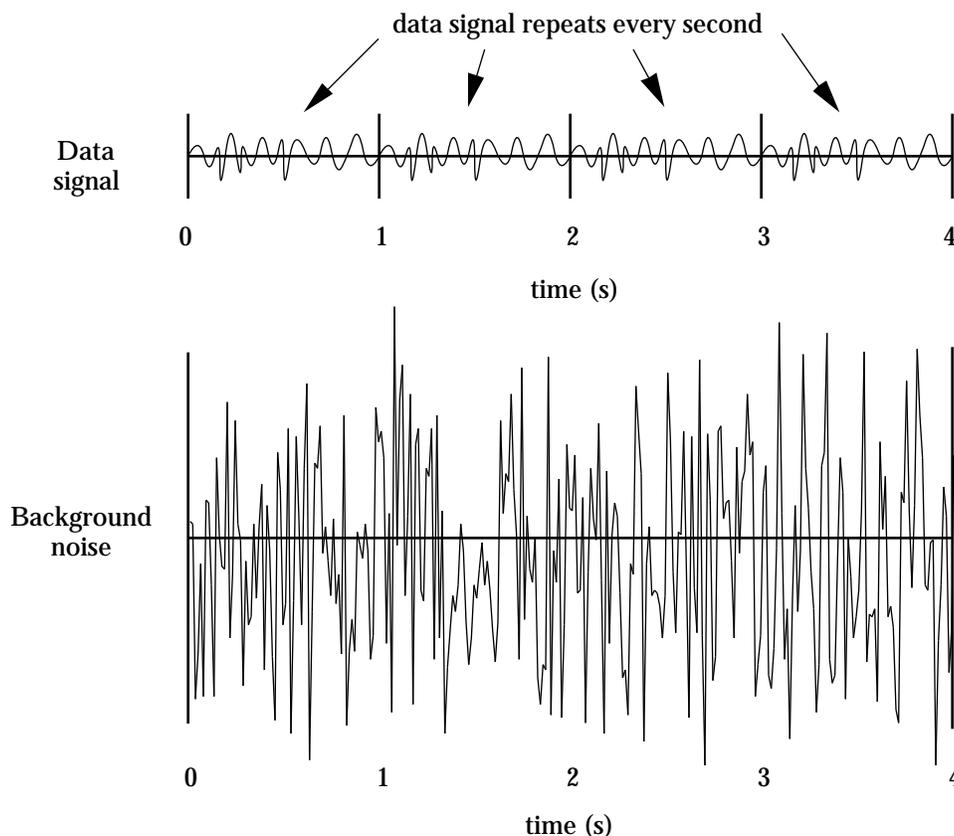
To do this, you perform the following steps:

- 1 receive and low-pass filter the radio signal (weak data signal plus background noise)
- 2 sample the filtered radio signal for exactly 100 s at 10,486 Hz ($2^{20} = 1,048,576$ samples)
- 3 take the FFT
- 4 subtract as much of the background noise as possible
- 5 recover one cycle of the data signal

Note 1: A periodic signal with repeat frequency f_r contains only frequencies $f = m f_r$

Note 2: If $a(t) = b(t) + c(t)$, then $\text{FFT}(a) = \text{FFT}(b) + \text{FFT}(c)$

Note 3: You may assume that the data signal does not change over 100 s.



Initials _____

- a.** (15 points) Describe (or sketch) the Fourier magnitudes M_n from the FFT in step 3 as a function of the frequency index n .
- b.** (5 points) To what frequency does the first Fourier magnitude M_1 correspond?
- c.** (10 points) Describe the gain vs. frequency relation that the low-pass filter should have.
- d.** (10 points) Explain whether a Hanning window would improve the recovered waveform.

Initials _____

- e. (20 points) Describe in detail how a computer program would implement steps 2, 3, 4, and 5.
(Note: there are over seven program steps)

Initials _____

Problem 4 (total 32 points):

After each of the four types of A/D converters listed below, write only the lettered characteristics that apply.

1. Successive approximation _____
 2. Tracking _____
 3. Dual slope (or integrating) _____
 4. Flash _____
-
- a. for N bits of resolution, conversion can take as many as 2^N operations
 - b. for N bits of resolution, conversion takes N operations
 - c. conversion is done continuously (i.e. one step for any change in input voltage)
 - d. requires a sample and hold for rapid, accurate conversion
 - e. limited to N = 10 bits at present
 - f. conversion accuracy does not depend on resistor accuracy
 - g. uses an internal D/A converter
 - h. uses one or more internal comparators
 - i. determines the output by counting pulses

Problem 5 (total 16 points):

After each of the four interfacing standards listed below, write only the lettered characteristics that apply:

1. RS-232 _____
 2. RS-422 _____
 3. IEEE-488 (GPIB) _____
 4. VME _____
-
- a. data transmitted serially (i.e. one bit at a time)
 - b. data transmitted in parallel (i.e. many bits transmitted simultaneously on as many wires)
 - c. can be implemented using existing telephone lines
 - d. allows up to 21 controllers (e.g. microprocessors) to interface with each other and with the circuits that they control
 - e. permits data transfer at or above 1000 bytes per second
 - f. permits data transfer at or above 1 million bytes per second
 - g. permits data transfer at or above 10 million bytes per second
 - h. serial data transfer using differential ± 3 volt signals on a twisted pair of wires

Initials _____

Equations, some of which you might find useful:

$$V(n) = V_{\text{ref}}^- + n \frac{V_{\text{ref}}^+ - V_{\text{ref}}^-}{2^N} = V_{\text{min}} + n \frac{V_{\text{max}} - V_{\text{min}}}{2^N - 1} \quad \left| \frac{V_{\text{out}}}{V_{\text{in}}} \right| = \frac{1}{\sqrt{1 + (f/f_c)^{2n}}}$$

$$n = \frac{V - V_{\text{ref}}^-}{V} + \frac{1}{2} \quad \text{INTEGER} \quad V(n-1, n) = V_{\text{ref}}^- + (n-0.5) V \quad V = \frac{V_{\text{ref}}^+ - V_{\text{ref}}^-}{2^N - 1}$$

$$G(a) = \frac{\exp\left(-\frac{1}{2} \frac{a - \mu}{\sigma}^2\right)}{\sqrt{2\pi} \sigma} \quad \mu = \bar{a} = \frac{1}{m} \sum_{i=1}^m a_i \quad \text{rms} = \sqrt{\frac{1}{m} \sum_{i=1}^m R_i^2} \quad R_i = a + b n_i - V_i$$

$$a = \frac{st - rq}{ms - r^2} \quad \text{and} \quad b = \frac{mq - rt}{ms - r^2} \quad \text{where} \quad r = n_i \quad s = n_i^2 \quad q = n_i V_i \quad t = V_i$$

$$\sigma^2 = \text{Var}(a) = \frac{1}{m-1} \sum_{i=1}^m R_i^2 = \frac{1}{m-1} \sum_{i=1}^m (a_i - \bar{a})^2 \quad \text{Var}(\bar{a}) = \text{Var}(a) / m$$

$$H(f) = \int_{-\infty}^{\infty} h(t) e^{-j2\pi ft} dt \quad h(t) = \begin{cases} A & \text{for } |t| \leq T_0/2 \\ 0 & \text{for } |t| > T_0/2 \end{cases} \quad H(f) = AT_0 \frac{\sin(\pi T_0 f)}{T_0 f}$$

$$H_n = \sum_{k=0}^{N-1} h_k e^{-j2\pi nk/N} \quad h_k = \sum_{n=0}^{N-1} \frac{H_n}{N} e^{+j2\pi nk/N}$$

$$M_n = |H_n| = \sqrt{\text{Re}(H_n)^2 + \text{Im}(H_n)^2} \quad \tan \theta_n = \text{Im}(H_n) / \text{Re}(H_n)$$

$$\text{For } h_k = \sum_{i=0}^{N-1} a_i \cos(2\pi ik/N) + b_i \sin(2\pi ik/N) \quad H_0 = Na_0 \quad H_n = (N/2)(a_n - jb_n)$$

$$f_{\text{max}} = f_s/2 \quad T = 1/f_s \quad S = NT \quad f = 1/S \quad h(t) = 0.5 [1.0 - \cos(2\pi t/S)]$$

$$y_i = A_1 x_{i-1} + A_2 x_{i-2} + \dots + A_M x_{i-M} + B_1 y_{i-1} + \dots + B_N y_{i-N}$$

If $a(t) = b(t)$ convolved with $c(t)$, then $\text{FFT}(a) = \text{FFT}(b)$ multiplied by $\text{FFT}(c)$

$$f_{\text{max}} = \frac{1}{2^{N+1} T} \quad e^{j\theta} = \cos \theta + j \sin \theta \quad V(t) = V(0) e^{-t/RC}$$

N =	8	9	10	11	12	13	14	15	16
2 ^N =	256	512	1,024	2,048	4,096	8,192	16,384	32,768	65,536

Have a pleasant summer!

